

**78mΩ, 600V, Super Junction N-Channel Power MOSFET**
**SRC60R078B**

## General Description

The Sanrise SRC60R078B is a high voltage power MOSFET, fabricated using advanced super junction technology. The resulting device has extremely low on resistance, low gate charge and fast switching time, making it especially suitable for applications which require superior power density and outstanding efficiency.

The SRC60R078B break down voltage is 600V and it has a high rugged avalanche characteristics.

The SRC60R078B is available in TO-263-2, TO-220F and TO-247 packages.

## Symbol

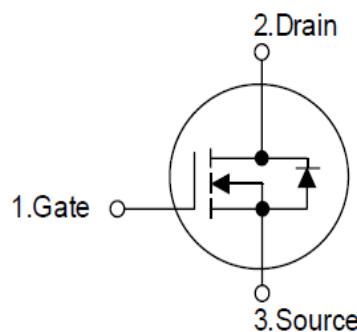


Figure 1 Symbol of SRC60R078B

## Features

- Ultra Low  $R_{DS(ON)}$  = 78mΩ @  $V_{GS}$  = 10V.
- Ultra Low Gate Charge,  $Q_g$ =90.3nC typ.
- Intrinsic Fast-Recovery Body Diode
- Fast switching capability
- Robust design with better EAS performance

## Package Type

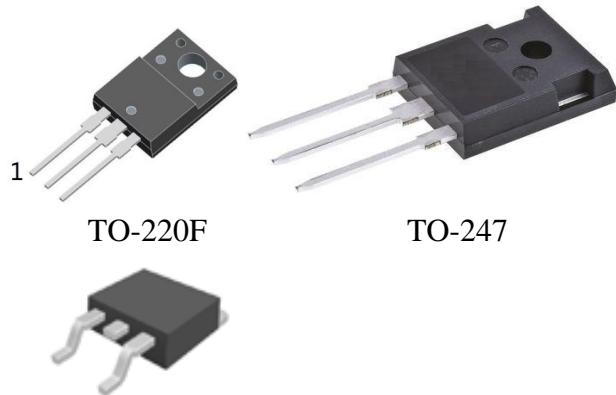
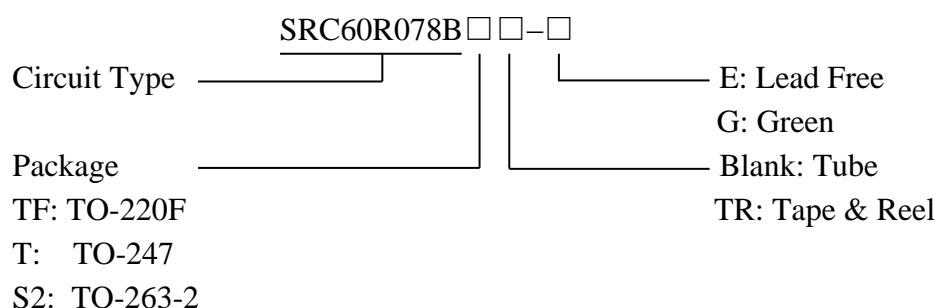


Figure 2 Package Types of SRC60R078B

## Application

- AC/DC Power Supply
- EV Charger
- PC / Server / Telecom
- Solar Inverter

## Ordering Information



Package	Part Number		Marking ID		Packing Type
	Lead Free	Green	Lead Free	Green	
TO-220F	SRC60R078BTF-E	SRC60R078BTF-G	SRC60R078BTFE	SRC60R078BTFG	Tube
TO-247	SRC60R078BT-E	SRC60R078BT-G	SRC60R078BTE	SRC60R078BTG	Tube
TO-263-2	SRC60R078BS2TR-E	SRC60R078BS2TR-G	SRC60R078BS2E	SRC60R078BS2G	Tape & Reel

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## Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DSS</sub>	600	V
Gate-Source Voltage	V <sub>GSS</sub>	±30	V
Continuous Drain Current T <sub>C</sub> =25°C	I <sub>D</sub>	42	A
T <sub>C</sub> =125°C		18.7	
Pulsed Drain Current (Note 2)	I <sub>DM</sub>	126	A
Avalanche Energy, Single Pulse (Note 3)	E <sub>AS</sub>	550	mJ
Avalanche Energy, Repetitive (Note 2)	E <sub>AR</sub>	0.6	mJ
Avalanche Current, Repetitive (Note 2)	I <sub>AR</sub>	4.5	A
Continuous Diode Forward Current	I <sub>S</sub>	42	A
Diode Pulse Current	I <sub>S.PULSE</sub>	126	A
MOSFET dv/dt Ruggedness, V <sub>DS</sub> <=480V	dv/dt	50	V/ns
Reverse Diode dv/dt, V <sub>DS</sub> <=480V, I <sub>SD</sub> <=I <sub>D</sub>	dv/dt	50	V/ns
Power Dissipation (TO-220F)	P <sub>tot</sub>	35.7	W
Power Dissipation (TO-247)	P <sub>tot</sub>	291	W
Operating Junction Temperature	T <sub>J</sub>	150	°C
Storage Temperature	T <sub>STG</sub>	-55 to 150	°C
Lead Temperature (Soldering, 10 sec)	T <sub>LEAD</sub>	260	°C

Note:

1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.  
Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature
3. I<sub>AS</sub> = 4.5A, V<sub>DD</sub> = 60V, R<sub>G</sub> = 25Ω, Starting T<sub>J</sub> = 25°C

## Thermal Resistance

Parameter	Symbol	Min	Typ	Max	Unit
Thermal resistance, Junction-to-Case	R <sub>thJC</sub>			3.5	°C /W
				0.43	
				0.43	
Thermal resistance, Junction-to-Ambient	R <sub>thJA</sub>			70	°C /W
				58	
				58	

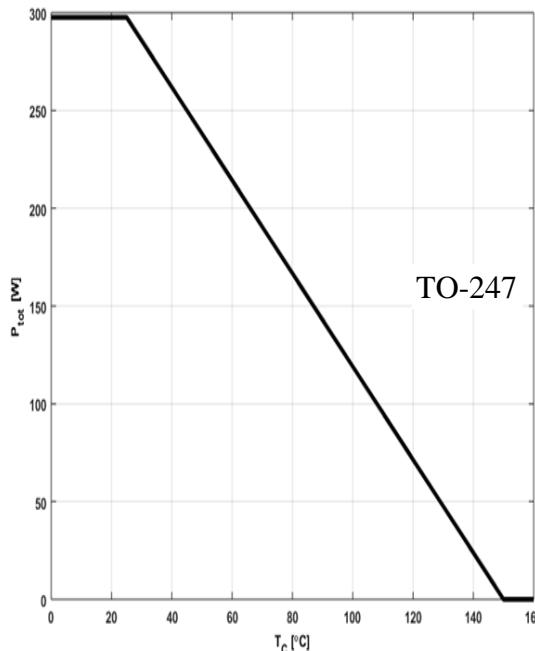
**78mΩ, 600V, Super Junction N-Channel Power MOSFET**
**SRC60R078B**
**Electrical Characteristics**
 $T_J = 25^\circ\text{C}$ , unless otherwise specified.

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Statistic Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	600			V
Zero Gate Voltage Drain Current	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}}=600\text{V}, \text{V}_{\text{GS}}=0\text{V}$			10	$\mu\text{A}$
Gate-Body Leakage Current	Forward	$\text{I}_{\text{GSSF}}$	$\text{V}_{\text{GS}}=30\text{V}, \text{V}_{\text{DS}}=0\text{V}$		100	nA
	Reverse	$\text{I}_{\text{GSSR}}$	$\text{V}_{\text{GS}}=-30\text{V}, \text{V}_{\text{DS}}=0\text{V}$		-100	nA
Gate Threshold Voltage	$\text{V}_{\text{GS}(\text{TH})}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=800\mu\text{A}$	3.0	4.0	5.0	V
Static Drain-Source On-Resistance	$\text{R}_{\text{DS}(\text{ON})}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=21\text{A}$		70	78	$\text{m}\Omega$
Gate Resistance	$\text{R}_G$	f=1MHz, Open Drain		0.7		$\Omega$
<b>Dynamic Characteristics</b>						
Input Capacitance	$\text{C}_{\text{ISS}}$	$\text{V}_{\text{DS}}=50\text{V}, \text{V}_{\text{GS}}=0\text{V},$ $f=1\text{MHz}$		3300		pF
Output Capacitance	$\text{C}_{\text{OSS}}$			136		
Reverse Transfer Capacitance	$\text{C}_{\text{RSS}}$			2.3		
Effective output capacitance, energy related <sup>NOTE5</sup>	$\text{C}_{\text{O(er)}}$	$\text{V}_{\text{GS}}=0\text{V},$ $\text{V}_{\text{DS}}=0\dots400\text{V}$		98.2		pF
Effective output capacitance, time related <sup>NOTE6</sup>	$\text{C}_{\text{O(tr)}}$			601		
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$\text{V}_{\text{DD}}=400\text{V}, \text{I}_D=21\text{A}$ $\text{R}_G=1.8\Omega, \text{V}_{\text{GS}}=10\text{V}$		24		ns
Rise Time	$t_r$			38		
Turn-off Delay Time	$t_{\text{d}(\text{off})}$			48		
Fall Time	$t_f$			10		
<b>Gate Charge Characteristics</b>						
Gate to Source Charge	$\text{Q}_{\text{gs}}$	$\text{V}_{\text{DD}}=480\text{V}, \text{I}_D=21\text{A}$ $\text{V}_{\text{GS}}=0 \text{ to } 10\text{V}$		22.9		nC
Gate to Drain Charge	$\text{Q}_{\text{gd}}$			45.6		
Gate Charge Total	$\text{Q}_g$			90.3		
Gate Plateau Voltage	$\text{V}_{\text{plateau}}$			6.5		V
<b>Reverse Diode Characteristics</b>						
Drain-Source Diode Forward Voltage	$\text{V}_{\text{SD}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_{\text{SD}}=21\text{A}$		0.9	1.1	V
Reverse Recovery Time	$t_{\text{rr}}$	$\text{V}_R=100\text{V}, \text{I}_F=21\text{A}$ $d\text{I}_F/dt=100\text{A}/\mu\text{s}$		158		ns
Reverse Recovery Charge	$\text{Q}_{\text{rr}}$			0.95		uC
Peak Reverse Recovery Current	$\text{I}_{\text{rrm}}$			12.0		A

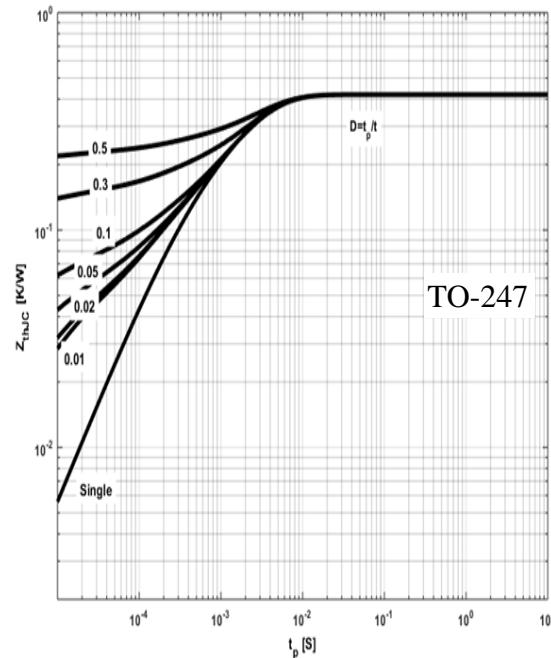
Note:

5.  $\text{C}_{\text{O(er)}}$  is a fixed capacitance that gives the same stored energy as  $\text{C}_{\text{OSS}}$  while  $\text{V}_{\text{DS}}$  is rising from 0 to 480V

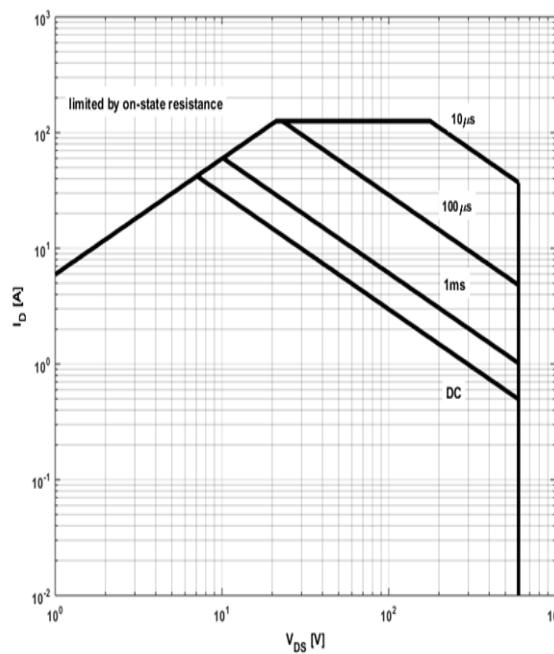
6.  $\text{C}_{\text{O(tr)}}$  is a fixed capacitance that gives the same charging time as  $\text{C}_{\text{OSS}}$  while  $\text{V}_{\text{DS}}$  is rising from 0 to 480 V

**78mΩ, 600V, Super Junction N-Channel Power MOSFET**
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**Typical Performance Characteristics**
**Figure 3: Power Dissipation**


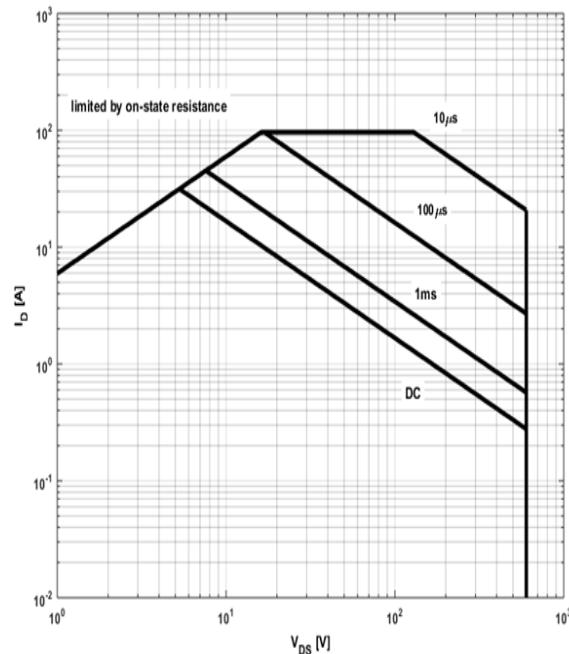
$$P_{tot} = f(T_c)$$

**Figure 4: Max. Transient Thermal Impedance**


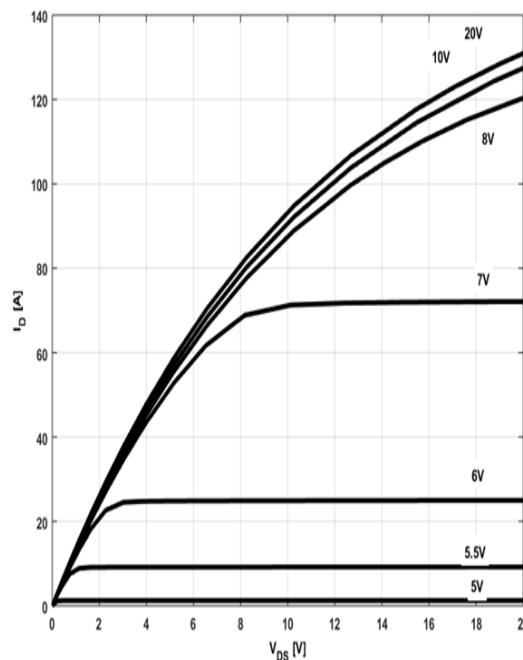
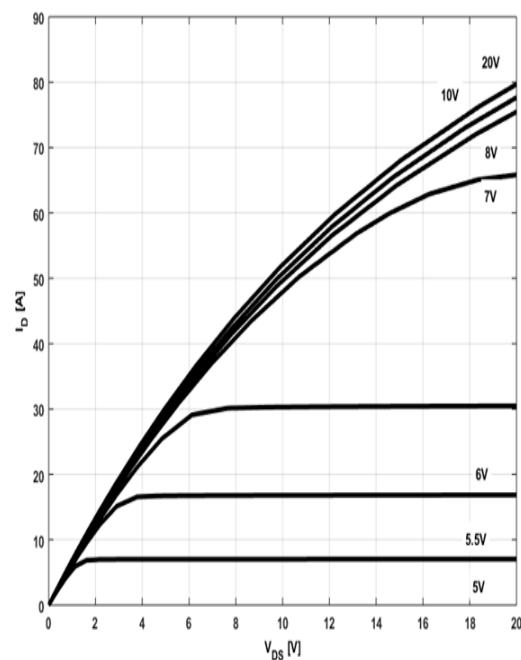
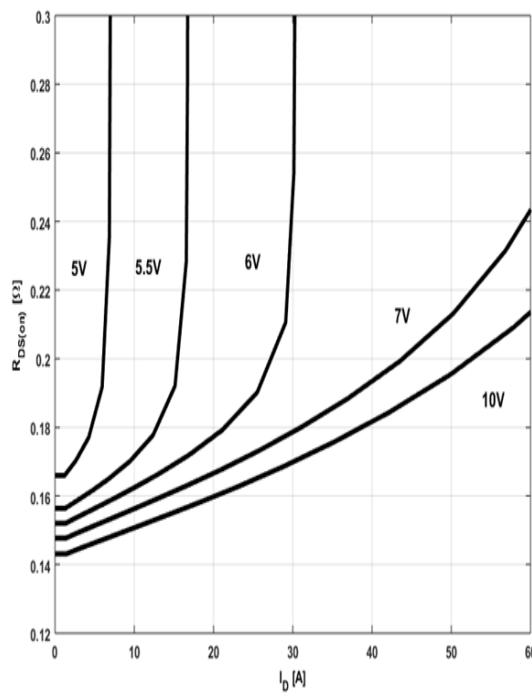
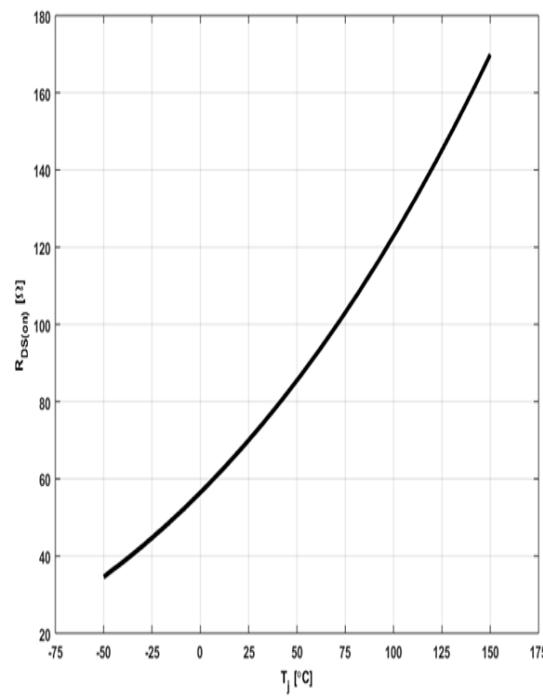
$$Z_{(thJC)} = f(t_p); \text{ parameter: } D = t_p/T$$

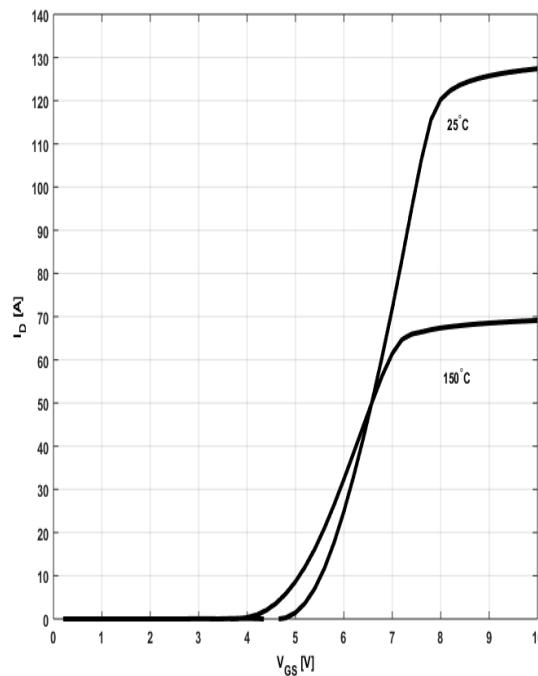
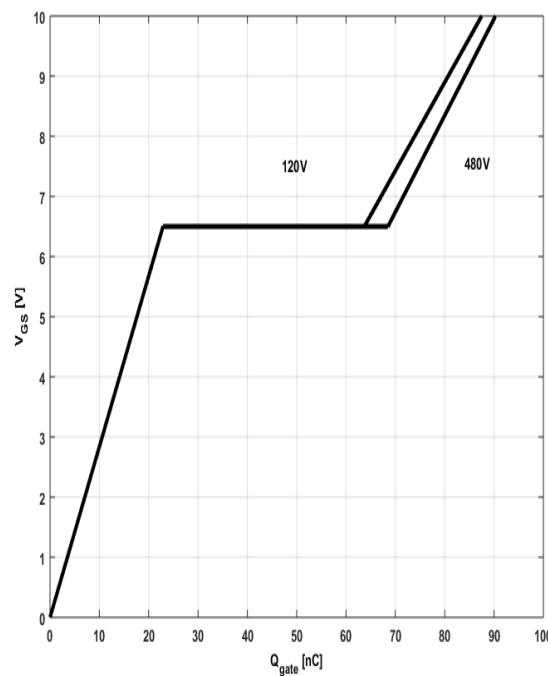
**Figure 5: Safe Operating Area**


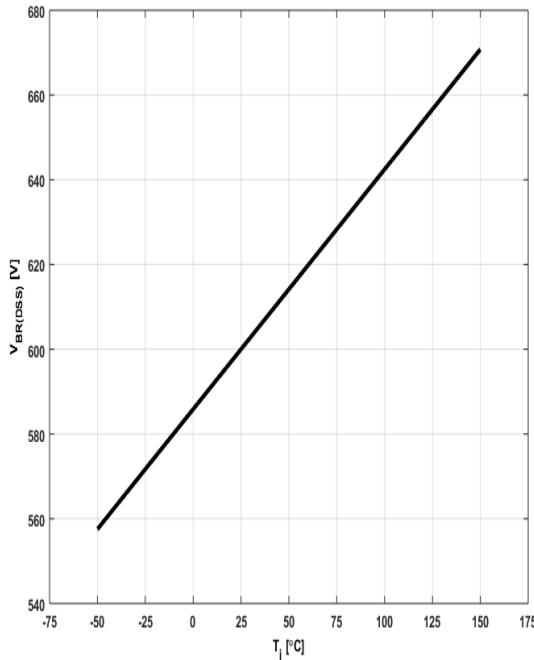
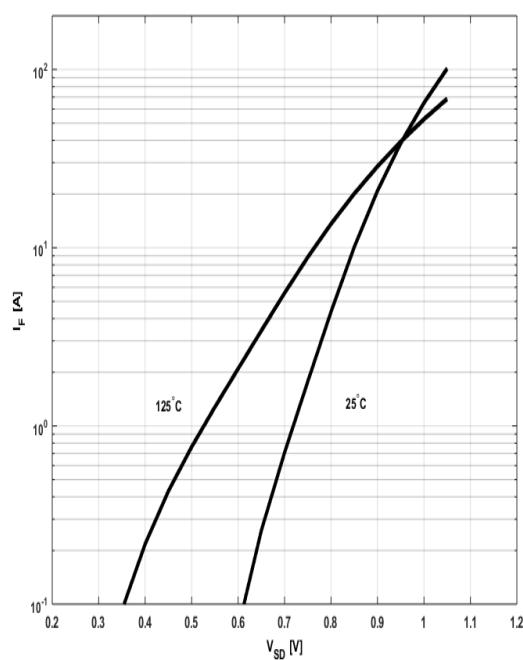
$$I_D = f(V_{DS}); T_c = 25^\circ\text{C}; V_{GS} > 7\text{V}; \text{ parameter } t_p$$

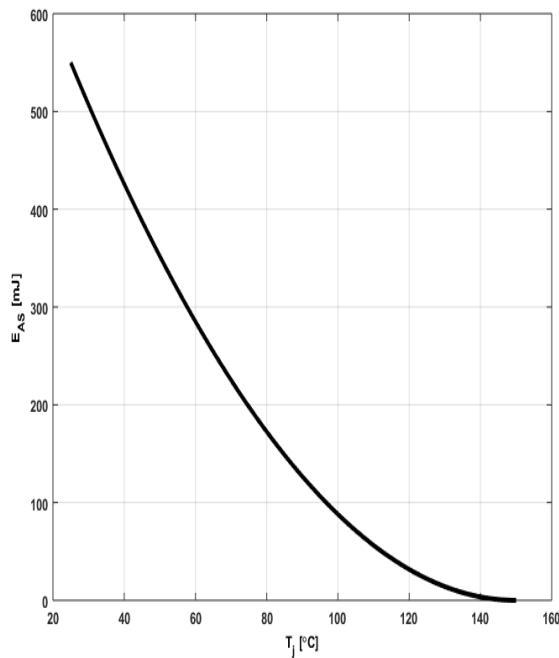
**Figure 6: Safe Operating Area**


$$I_D = f(V_{DS}); T_c = 80^\circ\text{C}; V_{GS} > 7\text{V}; \text{ parameter } t_p$$

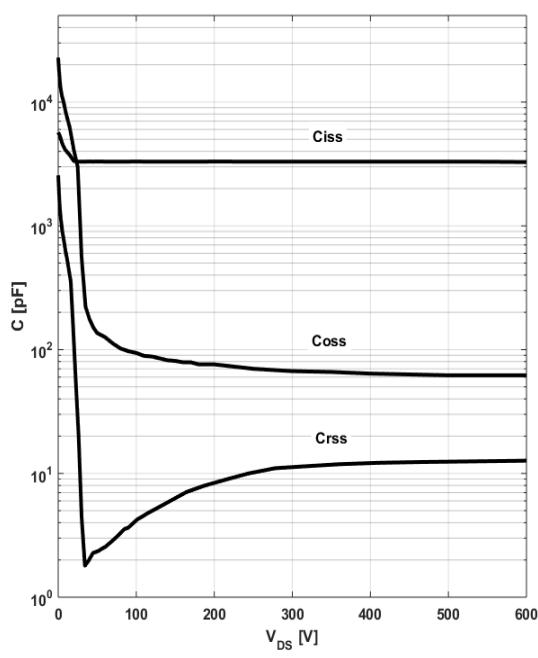
**78mΩ, 600V, Super Junction N-Channel Power MOSFET**
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**Figure 7: Typ. Output Characteristics**

 $I_D = f(V_{DS})$ ;  $T_j = 25^\circ\text{C}$ ; parameter:  $V_{GS}$ 
**Figure 8: Typ. Output Characteristics**

 $I_D = f(V_{DS})$ ;  $T_j = 125^\circ\text{C}$ ; parameter:  $V_{GS}$ 
**Figure 9: Typ. Drain-Source On-State Resistance**

 $R_{DS(ON)} = f(I_D)$ ;  $T_j = 125^\circ\text{C}$ ; parameter:  $V_{GS}$ 
**Figure 10: Typ. Drain-Source On-State Resistance**

 $R_{DS(ON)} = f(T_j)$ ;  $I_D = 21\text{A}$ ;  $V_{GS} = 10\text{V}$

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**Figure 11: Typ. Transfer Characteristics**

 $I_D = f(V_{GS})$ ;  $V_{DS} = 20\text{V}$ 
**Figure 12: Typ. Gate Charge**

 $V_{GS} = f(Q_{gate})$ ,  $I_D = 21\text{A}$  pulsed

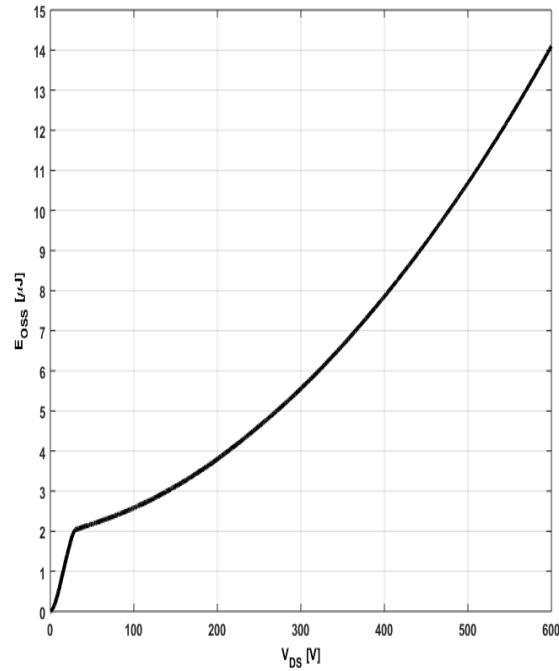
**Figure 13: Drain-Source Breakdown Voltage**

 $V_{BR(DSS)} = f(T_j)$ ;  $I_D = 10\text{mA}$ 
**Figure 14: Forward Characteristics of Reverse Diode**

 $I_F = f(V_{SD})$ ; parameter:  $T_j$

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**Figure 15: Avalanche Energy**


$$E_{AS}=f(T_j); I_D=4.5A; V_{DD}=60V$$

**Figure 16: Typ. Capacitances**


$$C=f(V_{DS}); V_{GS}=0; f=1MHz$$

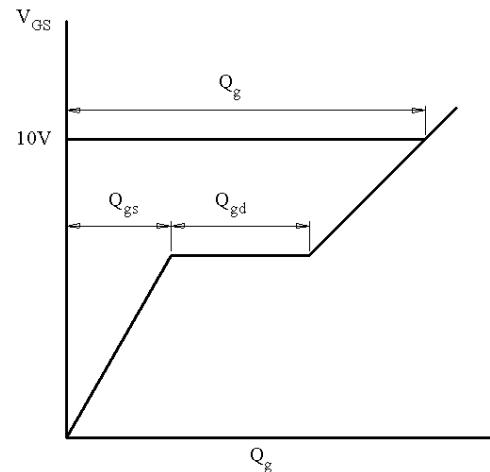
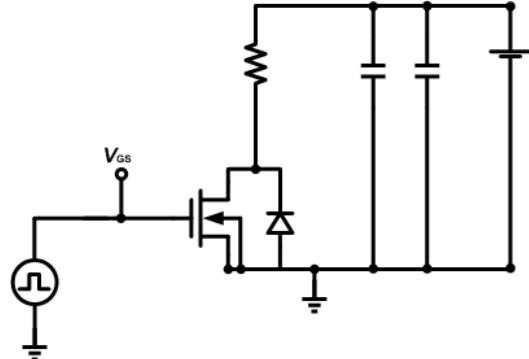
**Figure 17: Coss Stored Energy**


$$E_{oss}=f(V_{DS})$$

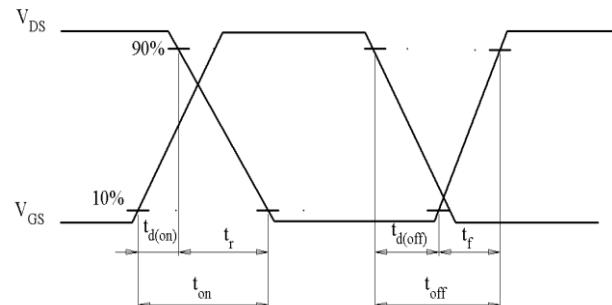
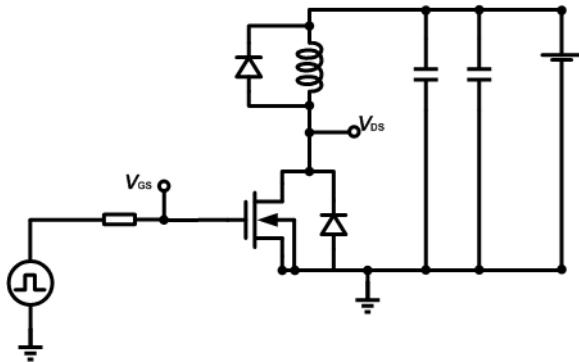


## Test Circuits

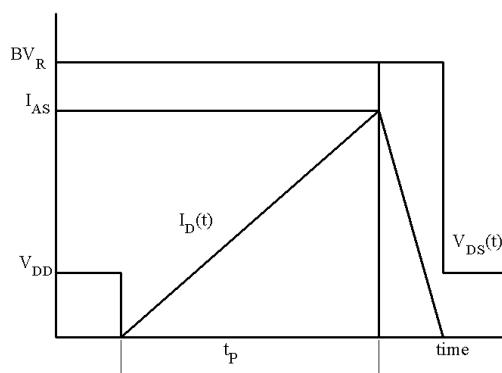
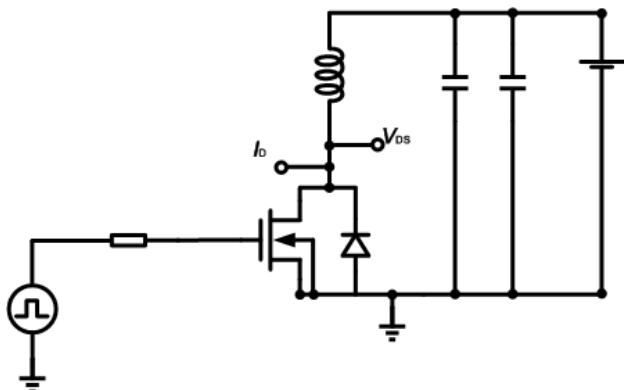
### 1. Gate Charge Test Circuit & Waveform



### 2. Switch Time Test Circuit

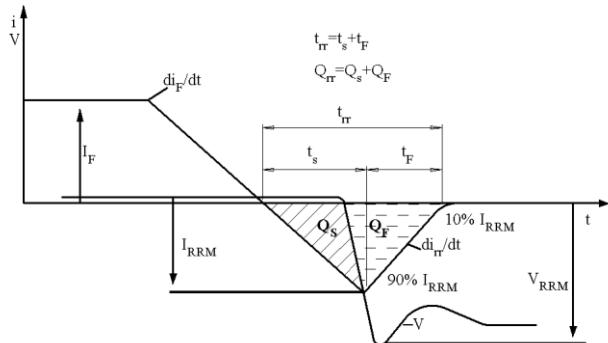
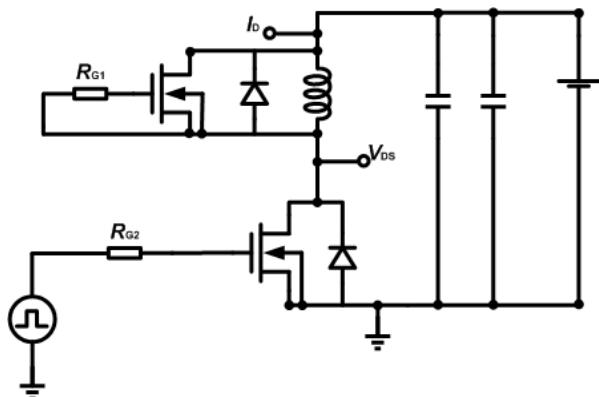


### 3. Unclaimed Inductive Switching Test Circuit & Waveforms



**78mΩ, 600V, Super Junction N-Channel Power MOSFET**
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#### 4. Test Circuit and Waveform for Diode Characteristics

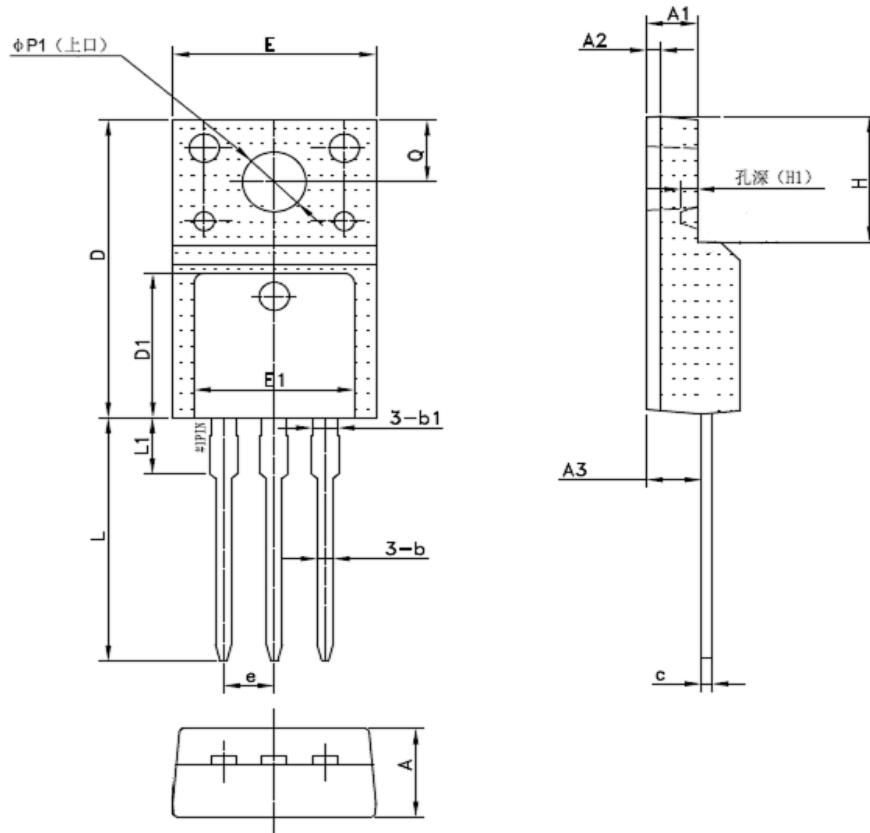




## Mechanical Dimensions

TO-220F

Unit: mm



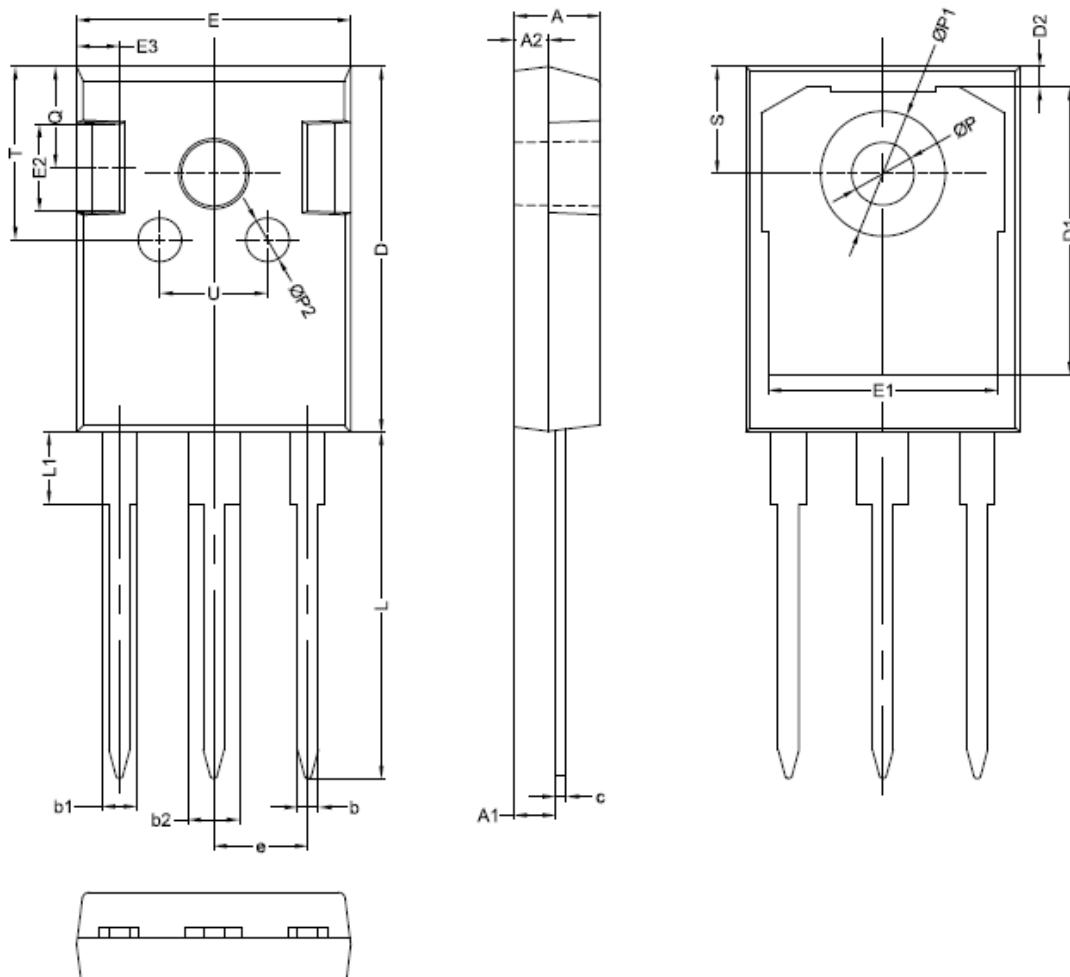
Symbol	Dimensions(mm)		
	Min.	Typ.	Max.
A	4.30	4.70	4.90
A1	2.34	2.54	2.90
A2	-	0.70	-
A3	2.56	2.76	2.96
b	0.55	-	0.95
b1	-	1.28	-
c	0.42	0.50	0.70
D	14.70	-	16.07
D1	-	7.70	-
E	9.96	10.16	10.36
E1	-	8.00	-
e	2.54(BSC)		
H	-	6.70	-
(H1)	-	(0.81)	-
L	12.48	12.98	13.50
L1	-	2.93	-
ΦP1	-	3.18	-
Q	2.90	3.30	3.50



## Mechanical Dimensions (Continued)

TO-247

Unit: mm



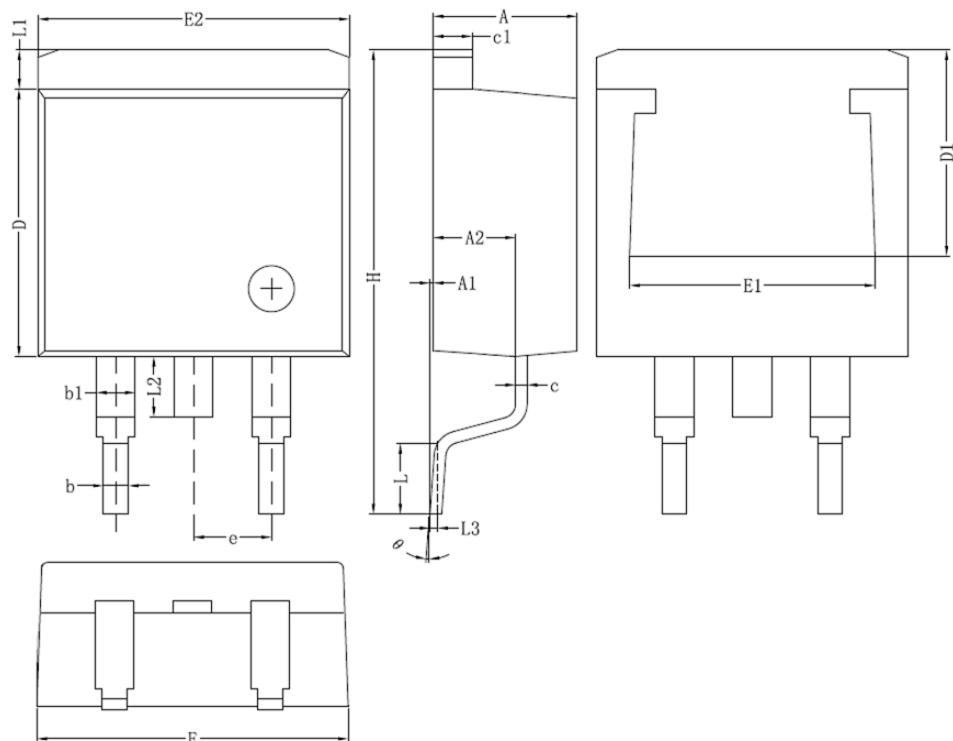
Symbol	Dimensions(mm)			Symbol	Dimensions(mm)		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.80	5.00	5.20	E2	-	5.00	-
A1	2.21	2.41	2.61	E3	-	2.50	-
A2	1.90	2.00	2.10	e	5.44(BSC)		
b	1.10	1.20	1.35	L	19.42	19.92	20.42
b1	-	2.00	-	L1	-	4.13	-
b2	-	3.00	-	P	3.50	3.60	3.70
c	0.55	0.60	0.75	P1	-	-	7.40
D	20.80	21.00	21.20	P2	-	2.50	-
D1	-	16.55	-	Q	-	5.80	-
D2	-	1.20	-	S	6.05	6.15	6.25
E	15.60	15.80	16.00	T	-	10.00	-
E1	-	13.30	-	U	-	6.20	-



## Mechanical Dimensions

TO-263-2

Unit: mm



Symbol	Dimensions(mm)		
	Min.	Typ.	Max.
A	4.30	4.60	4.85
A1	0.00	0.10	0.25
A2	2.59	2.69	2.89
b	0.70	0.81	0.96
b1	-	1.27	-
c	0.36	0.40	0.61
c1	1.15	1.27	1.40
D	8.55	-	9.40
D1	6.40	-	-
E	9.80	10.10	10.31
E1	7.60	-	-
E2	9.80	10.00	10.20
e	2.54(BSC)		
H	14.70	15.20	16.00
L	2.00	2.30	2.84
L1	1.00	1.27	1.40
L2	-	-	2.20
L3	-	0.25	-
θ	0°	-	8°



Sanrise Technology Limited Company

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